



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Michael Peters

Application No.: 10/782,386

Filed: February 18, 2004

For: METHOD AND CIRCUIT FOR INCREASING THE MEMORY ACCESS

SPEED OF AN ENHANCED SYNCHRONOUS SDRAM

Examiner: Unassigned

Art Unit: 2818

INFORMATION DISCLOSURE

STATEMENT UNDER 37 CFR §1.97 and

§1.98

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

The references cited on attached form PTO/SB/08A and PTO/SB/08B are being called to the attention of the Examiner. In accordance with 37 CFR §1.98(d), copies of references AA-AP can be found in Application No. 09/828,488, filed 04/05/2001 (Attorney Docket No. 022193-042800US). A copy of reference AR is enclosed.

It is respectfully requested that the cited references be expressly considered during the prosecution of this application, and the references be made of record therein and appear among the "references cited" on any patent to issue therefrom.

As provided for by 37 CFR 1.97(g) and (h), no inference should be made that the information and references cited are prior art merely because they are in this statement and no representation is being made that a search has been conducted or that this statement encompasses all the possible relevant information.

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Applicant believes that <u>no fee is required</u> for submission of this statement.

However, if a fee is required, the Commissioner is authorized to deduct such fee from the undersigned's Deposit Account No. 20-1430. Please deduct any additional fees from, or credit any overpayment to, the above-noted Deposit Account.

Respectfully submitted,

Patrick R. Jewik Reg. No. 40,456

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PTO/SB/08A-B (08-03) Substitute for form 1449B/PTO Complete if Known 10/782,386 Application Number INFORMATION DISCLOSURE February 18, 2004 Filing Date STATEMENT BY APPLICANT Peters, Michael First Named Inventor Art Unit 2818 (use as many sheets as necessary) Examiner Name Unassigned 022193-042810US Attorney Docket Number Sheet

U.S. PATENT DOCUMENTS+							
Examiner Initials*	Cite No.1	Document Number Number Kind Code ² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear		
	AA	US-5,025,421	06-18-1991	Cho			
	AB	US-5,226,009	07-06-1993	Arimoto			
	AC	US-5,301,162	04-05-1994	Shimizu			
	AD	US-5,421,000	05-30-1995	Fortino et al.			
	AE	US-5,528,552	06-18-1996	Kamisaki			
	AF	US-5,680,363	10-21-1997	Dosaka et al.			
	AG	US-5,706,244	01-06-1998	Shimizu			
	AH	US-5,721,862	02-24-1998	Sartore et al.			
	Al	US-5,881,009	03-09-1999	Tomita			
	AJ	US-5,887,272	03-23-1999	Sartore et al.			
	AK	US-5,900,011	05-04-1999	Saulsbury et al.			
	AL	US-5,950,220	09-07-1999	Quach			
	AM	US-6,295,593 B1	09-25-2001	Hsu et al.			
	AN	US-6,526,471 B1	02-25-2003	Shimomura et al.			
	AO	US-6,546,453 B1	04-08-2003	Kessler et al.			

NON PATENT LITERATURE DOCUMENTS							
Examiner Initials *	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²				
	AP	DOSAKA, K. et al.; "A 100-MHz 4-Mb Cache DRAM with Fast Copy-Back Scheme"; IEEE Journal of Solid-State Circuits; November, 1992.					
	AQ	Enhanced Memory Systems, Inc.; "Preliminary Data Sheet, - 64Mbit - Enhanced SDRAM, 8Mx8, 4Mx16 ESDRAM"; pp. 1-33; 2000 Enhanced Memory Systems, 1850 Ramtron Drive, Colorado Springs, Colorado, (800) 545-DRAM, http://www.edram.com.					

Examiner	 Date	
Signature	Considered	

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Applicant's unique citation designation number (optional). Applicant is to place a check mark here if English language Translation is attached.